# The Future Is Semiconductor Webinar/Biz-meeting

Strengthening the business cooperation in areas of semiconductor & semiconductor equipment between Korea and the Netherlands

#### Webinar

Oct 11 WED, 2023 08:30-10:30 AM (EU)

### **Contents of Webinar**

- EUV, advanced packaging and information about Semiconductor clusters by Korean and Dutch (or European) research centers/ associations/organizations/global companies
- Possibilities about cooperation between KR-NL & Suggestions for strengthening cooperation

#### **Program Session**

- 1. Development of Semiconductor industry in the Netherlands and Korea
- 2. Semiconductor Process I (Development of EUV)
- 3. Semiconductor Process II (Development of Advanced packaging)

**Biz-Meeting (Online)** 

Oct 2 - Oct 31, 2023

#### **1:1 Business Meeting**

- 1:1 business meeting between Korean and Dutch companies for discussing purchasing, technology or investment cooperation
- Meeting arrangement will be done by KOTRA on the basis of the application (application fee: Free)

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\*Click here for registration

Korea Trade-Investme Promotion Agency





## **Webinar Overview**

Time(Dutch Time)	Subject	Speaker
08:30 ('1)	Opening remarks	KOTRA
08:31 ('6)	Congratulatory remarks	<b>Mr. HyoungChan Choe</b> (Korean Embassy in the Netherlands) <b>Mr. Peter van der Vliet</b> (Dutch Embassy in Korea)
Session 1: Develo	pment of Semiconductor industry in the Net	therlands and Korea
08:37 ('15)	Semiconductor cluster in Korea	Ms. MinAe Noh Gyeonggi province
08:52 ('15)	Semiconductor cluster in the Netherlands	<b>Mr. Toine Cleophas</b> (Brabant/BOM)
Sessi	on 2: Semiconductor Process I (Development	t of EUV)
09:07 ('15)	Development of EUV in Korea	<b>Prof. Jinho Ahn</b> (Hanyang Institute of Smart Semiconductor (HY-ISS))
09:22 ('15)	Development of EUV in the Netherlands	Dr. Wim van der Zande (ARCNL)
Session 3: Sen	niconductor Process II (Development of Adv	anced packaging)
09:37 ('15)	Development of Advanced packaging in Korea	Mr. Lewis (Insoo) Kang (NEPES)
09:52 ('15)	Additive Manufacturing enabling Breakthroughs in Heterogeneous Integration of Semiconductor and Integrated Photonics Packaging	Mr. Mark Luke Farrugia (CITC)
10:07 ('8)	Q&A	

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